

Amendments to the Drawings:

The attached drawing sheets show changes to FIGs. 1, 5 and 6.

Attachment: Replacement sheets

Annotated sheets showing changes.



100

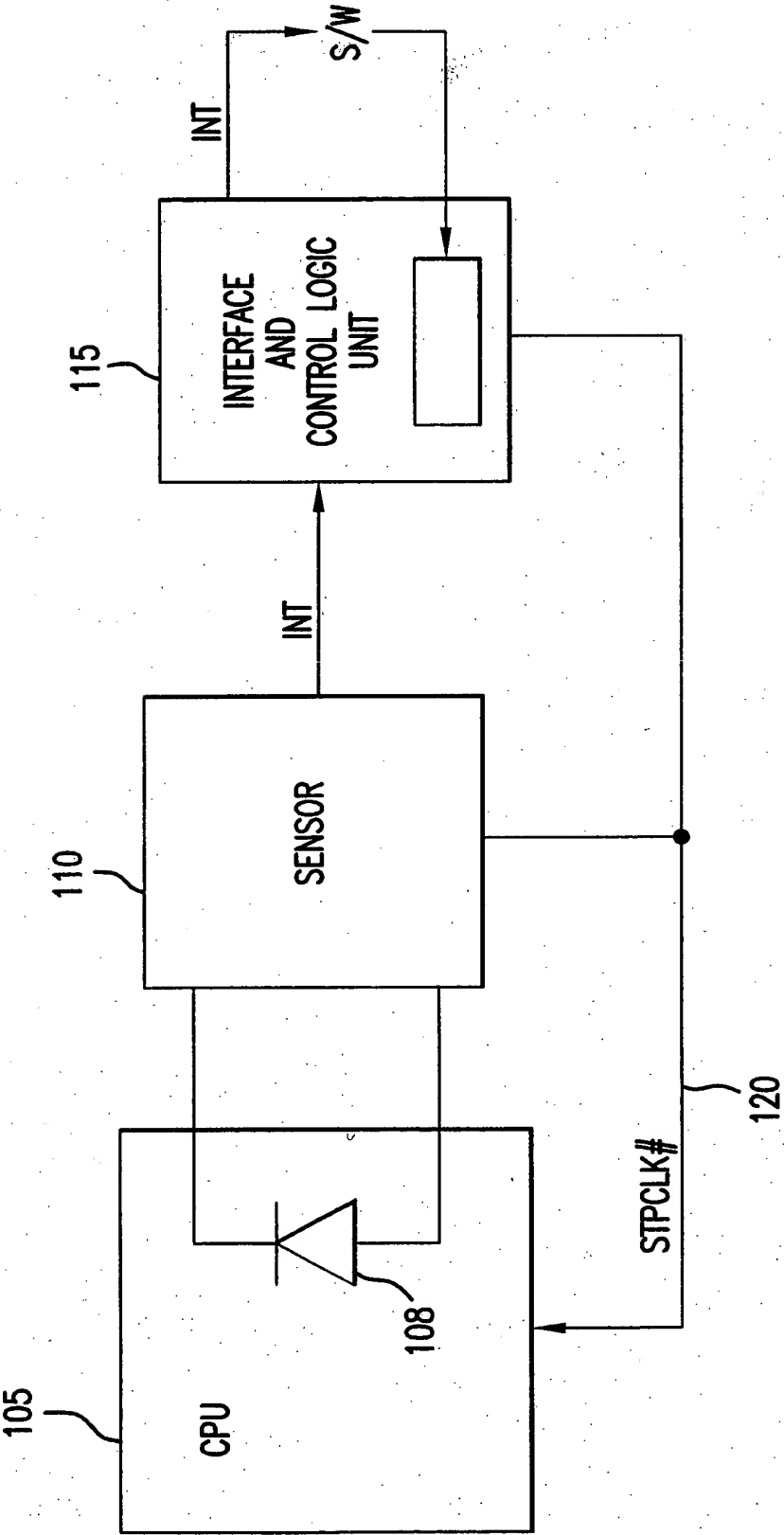


FIG. 1

PRIOR ART



500

FFRL_EN	dT/dt	THERMAL TEMPERATURE	CURRENT LOGIC STATE	PREV. LOGIC STATE
0 (NOT NEAR MAXIMAL THERMAL LIMIT)	NOT CARE	NOT CARE	POWER DOWN	POWER DOWN
0 (NOT NEAR MAXIMAL THERMAL LIMIT)	NOT CARE	NOT CARE	POWER DOWN	WAIT
0 (NOT NEAR MAXIMAL THERMAL LIMIT)	NOT CARE	NOT CARE	POWER DOWN	ACTIVE
1 (NEAR MAXIMAL THERMAL LIMIT)	>0.2 (SLOW RATE)	<MAX. TEMPERATURE-- δt	WAIT	POWER DOWN
1 (NEAR MAXIMAL THERMAL LIMIT)	<0.2 (SLOW RATE)	<MAX. TEMPERATURE-- δt	WAIT	ACTIVE
1 (NEAR MAXIMAL THERMAL LIMIT)	>0.2 (SLOW RATE)	<MAX. TEMPERATURE-- δt	WAIT	WAIT
1 (NEAR MAXIMAL THERMAL LIMIT)	NOT CARE	>MAX. TEMPERATURE-- δt	ACTIVE	POWER DOWN
1 (NEAR MAXIMAL THERMAL LIMIT)	NOT CARE	>MAX. TEMPERATURE-- δt	ACTIVE	WAIT
1 (NEAR MAXIMAL THERMAL LIMIT)	NOT CARE	>MAX. TEMPERATURE-- δt	ACTIVE	ACTIVE

FIG.5



LOGIC STATES DIAGRAM OF FAST FREQUENCY REDUCTION
LOGIC (FFRL)

600

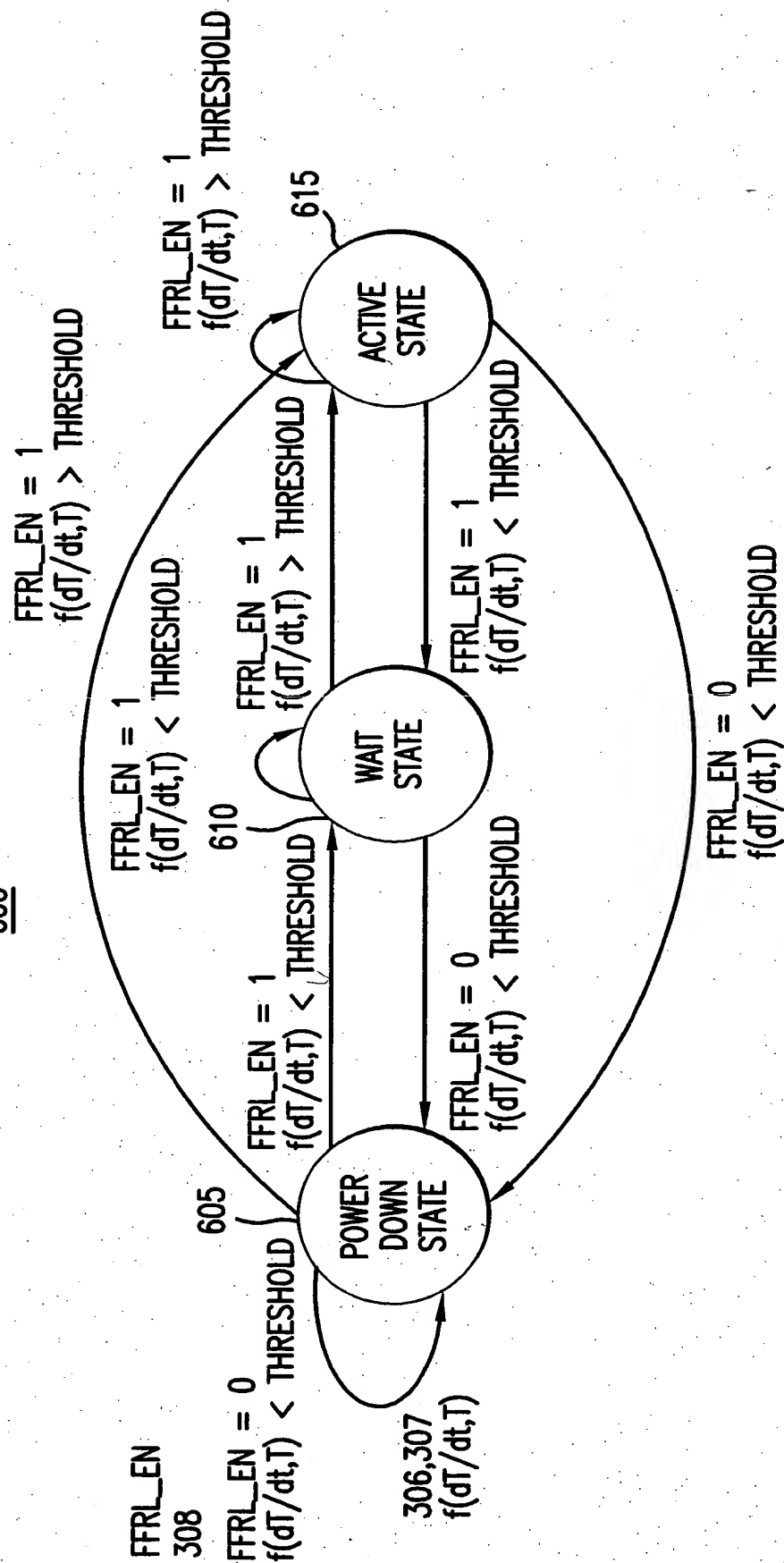


FIG. 6